



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Darrell K. Cox

Serial No: 10/026,342

Filed: December 21, 2001

For: Method and Apparatus for Computing  
Reed-Solomon Error Magnitudes

TI-29876

Art Unit: 2133

Examiner: R. Stephen Dildine, Jr.

Conf. No.: 8123

Not. Of Allow: March 9, 2005

**REPLACEMENT DRAWINGS TRANSMITTAL LETTER**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

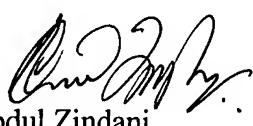
Sir:

MAILING CERTIFICATE UNDER 37 C.F.R. §1.8(a)  
I hereby certify that the above correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on April 6, 2005.

  
Robin E. Barnum

Per the *Notice of Allowability*, please find enclosed the Formal Drawings for the subject case. These drawings are being submitted to replace Formal Drawings previously submitted October 1, 2004, but inadvertently not labeled "Replacement Sheet". This set has been labeled correctly.

Respectfully submitted,

  
Abdul Zindani  
Attorney for Applicant  
Reg. No. 46,091

Texas Instruments Incorporated  
P.O. Box 655474, MS 3999  
Dallas, TX 75265  
(972) 917-5137